**Ph.D. COMMON ENTRANCE TEST**

**SUBJECT – ELECTRICAL & ELECTRONICS ENGINEERING**

**Roll No:**

**PART B**

**Duration: 60 minutes Maximum Marks: 50**

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| **Instructions:**1. **This entrance test question paper is not to be taken out of the examination hall**
2. **Question paper consists of Section A and Section B**
3. **Section A consists of 30 MCQs carrying 1 Mark each. Write the Alphabet of the correct answer in the space given.**
4. **Section B consists of Descriptive questions carrying 5 marks each. Restrict your answer to 500 words. Additional plain sheets have been attached to the question paper to answer Section B**
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**SECTION – A**

**Answer the following questions by writing the Alphabet of the correct answer in the Box given: 30 X 1 = 30**

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|  | If 1 A current flows in a circuit, the number of electrons flowing through this circuit is 1. 0.625 × 10^19
2. 1.6 × 10^19
3. 1.6 × 10 ^ (- 19)
4. 0.625 × 10^ (- 19)

  |  |
|  | If P is the power of a star connected system then what will be the power of an equivalent delta connected system? A. PB. 3PC. P/3D. 3/P |  |
|  | KVL works on the principle of 1. law of conservation of charge.
2. law of conservation of energy.
3. both.
4. None of the above.
 |  |
|  | A balanced delta connected load has an impedance of 9∠30o Ω per phase. What is the impedance per phase of its equivalent star?1. 27∠30o Ω
2. 30∠30o Ω.
3. 3∠30o Ω.
4. 20∠30o Ω.
 |  |
|  | The feedback factor of a Wien bridge oscillator using Op-Amp is1. ⅓
2. ¼
3. ½
4. 1
 |  |
|  | If the emitter resistance decreases, the1. Q point moves up.
2. Collector current decreases.
3. Q point stays where it is.
4. Current gain increases.
 |  |
|  | The leakage current in a pn junction is in order of1. A
2. kA
3. µA
4. mA
 |  |
|  | The base-emitter voltage of the second approximation of a transistor is1. 0 V
2. 0.3 V
3. 0.7 V
4. 1 V
 |  |
|  | Consider a 200V, 25kW, 30A DC machine lap connected with armature resistance of 0.4 ohms. If the machine is later wave wound, then the developed power is?1. 25kW
2. 12.5kW
3. 20kW
4. 50kW
 |  |
|  | A synchronous motor is operated at a bus voltage of 1.5 pu and drawing 1.0 pu at zpf leading current. Its synchronous reactance is 0.4 pu. What is the excitation emf of the motor?1. 2.4 p.u.
2. 1.5 p.u.
3. 1.0 p.u.
4. 1.9 p.u.
 |  |
|  | The condition for maximum power output for an alternator having its load impedance as (2+j) ohms is?1. 26.56°
2. 63.43°
3. 90°
4. 116.56°
 |  |
|  | 4.With constant E,V,Z, the maximum input power can be obtained at load angle of value\_\_\_\_\_\_\_1. 180-θ
2. 90+θ
3. θ
4. 180
 |  |
|  | For the unity feedback control system shown in the figure, the open-loop transfer function G(s) given as1. 0
2. 1.0
3. 0.5
4. Infinity
 |  |
|  | Which of the following statements is correct for a system with gain margin close to unity or a phase margin close to zero?1. The system is relatively stable
2. The system is highly stable
3. The system is highly oscillatory
4. None of the above
 |  |
|  | The number of root loci branches which do not terminate at zero is given by,1. The number of zeros
2. The number of poles
3. The number of zeros + The number of poles
4. The number of zeros - The number of poles
 |  |
|  | When the moving coil in a Dynamometer type wattmeter deflects \_\_\_\_\_\_\_\_1. pointer doesn’t move
2. current flows
3. voltage is generated
4. pointer moves
 |  |
|  | In a DVM, a signal conditioning circuit is used \_\_\_\_\_\_\_\_1. to bring current to a suitable limit
2. to bring resistance to a suitable limit
3. to bring power to a suitable limit
4. to bring voltage to a suitable limit
 |  |
|  | The sweep generator of a CRO is used to produce1. Saw tooth voltage for the horizontal deflection of electron beam
2. Sinusoidal voltage for the vertical deflection of electron beam
3. Saw tooth voltage for the vertical deflection of electron beam
4. Sinusoidal voltage for the horizontal deflection of electron beam
 |  |
|  | Which of the following is a type of digital logic circuit?1. Combinational logic circuits
2. Sequential logic circuits
3. Both Combinational & Sequential logic circuits
4. None of the mentioned
 |  |
|  | Which gates in Digital Circuits are required to convert a NOR-based SR latch to an SR flip-flop?1. Two 2 input AND gates
2. Two 3 input AND gates
3. Two 2 input OR gates
4. Two 3 input OR gates
 |  |
|  | A negative current is used to turn off which of the following powerelectronic device1. SCR
2. GTO
3. MOSFET
4. IGBT
 |  |
|  | In a simple square wave inverter which of the following cannot becontrolled1. Voltage
2. Current
3. Frequency
4. Switching Time
 |  |
|  | Which of the following converter supports four quadrant operation of aDC drive1. Fully Controlled Rectifier
2. Class D Chopper
3. Class E Chopper
4. Semi Converter
 |  |
|  | Which of the following SCR rectifier circuits will have a poor power factor.1. Rectifier with Resistive load and firing angle of 45 degrees
2. Rectifier with R-L load and firing angle of 30 degrees
3. Rectifier with Resistive load and firing angle of 90 degrees
4. Rectifier with RL Load and firing angle of 60 degrees
 |  |
|  | A single-phase load is supplied by a single-phase voltage source. If the current flowing from the load to the source is theA and if the voltage at the load terminals is then1. load absorbs real power and delivers reactive power.
2. load absorbs real power and absorbs reactive power.
3. load delivers real power and delivers reactive power.
4. load delivers real power and absorbs reactive power
 |  |
|  | Which one of the following is not true in HDFC transmission?1. Corona loss is much more in HVDC transmission.
2. The power transmission capability of bipolar line is almost the same as that of single-circuit ac line.
3. HVDC link can operate between two AC systems whose frequencies need not be equal.
4. There is no distance limitation for HVDC transmission by underground cable.
 |  |
|  | In the following network, the voltage magnitudes at buses are equal to 1 p.u. The voltage phase angles are very small, and the line resistances are negligible. All the line reactances are equal to j1Ω. The voltage phase angles in rad at buses 2 and 3 are1. θ 2 = −0.1, θ 3 = −0.2
2. θ 2 = 0, θ 3 = −0.1
3. θ 2 = 0.1, θ 3 = 0.1
4. θ 2 = 0.1, θ 3 = 0.2
 |  |
|  | If the base impedance and the line-to line base voltage are 100 ohms and 100kV respectively, then the real power in MW delivered by the generator connected at the slack bus is1. -10
2. 0
3. 10
4. 20
 |  |
|  | Which of the following systems is time invariant?1. y(t) = x(2t) + x(t)
2. y(t) = x(t) + x(1-t)
3. y(t) = -x(t) + x(1-t)
4. y(t) = x(t) + x(t-1)
 |  |
|  30 State whether the integrator system is stable or not.1. Unstable
2. Stable
3. Partially Stable
4. All of the mentioned
 |
|  |  |  |

**Section - B**

**Answer any four questions (Each question carry 5 marks 4\*5 = 20**

1. Find the current passing through the 5 ohm resistor (encircled in red color) and comment on the method which is easier compared to difficult ones (Mesh Analysis and Nodal Analysis only).



2. Illustrate in detail the role of Electrical Machines in Steel Rolling Mills with neat diagram.

3. The first two rows in the Routh table for the characteristic equation of a certain closed

loop control system is given as



The range of K for which the system is stable is

1. -2.0<K<0.5
2. 0<K<0.5
3. 0<K<Infinity
4. 0.5<K<Infinity

4. A synchronous generator is connected to an infinite bus with excitation voltage Ef = 1.3 pu. The generator has a synchronous reactance of 1.1 pu and is delivering real power (P) of 0.6 pu to the bus. Assume the infinite bus voltage to be 1.0 pu. Neglect stator resistance. The reactive power (Q) in pu supplied by the generator to the bus under this condition is\_\_\_\_\_\_\_\_\_\_\_\_

5. Describe the classifications of signals and systems.

6. Draw the circuit of a dual converter and explain how it can be operated as a cyclo

converter with suitable illustrations.

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